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#9/1/03
12-18-03
WillIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Kenneth Shepard

Serial No. : 09/591,270 Examiner : Ayal I. Sharon

Filed : June 9, 2000 Art Unit : 2123

For : METHODS FOR ESTIMATING THE BODY VOLTAGE OF
DIGITAL PARTIALLY DEPLETED SILICON-ON-
INSULATOR CIRCUITS

RESPONSE TO OFFICE ACTION

I hereby certify that this paper is being deposited
with the United States Postal Service as first class mail in
an envelope addressed to: Commissioner for Patents, P.O.
Box 1450, Arlington, VA 22313-1450

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Technology Center 2100

12/4/03
Date of DepositPaul A. Ragusa
Attorney Name38,587
PTO Registration No[Signature]
Signature12/4/03
Date of Signature

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450
Dear Sir:

Applicant respectfully petitions for a one-month extension of time to
respond to the office action mailed on August 4, 2003. A check is enclosed for the fee